

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	Avoiding Excessive Cross-terminal Voltages of Low Voltage Transistors Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages							
Application Number : Date : First Named Applicant: Mr. Bhupendra SHARMA Confirmation Number: Attorney Docket Number: TI-38242								
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Mr. Narendra Reddy Thappeta Registered Number: 41416</td><td>/Narendra Reddy Thappeta/</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Mr. Narendra Reddy Thappeta Registered Number: 41416	/Narendra Reddy Thappeta/	Attorney
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Mr. Narendra Reddy Thappeta Registered Number: 41416	/Narendra Reddy Thappeta/	Attorney						

Documents being submitted:	Files
us-fee-sheet	EFStxn-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-request	EFStxn-usrequ.xml us-request.dtd us-request.xsl
application-body	ABXspectxn108.xml application-body.dtd Image1.tif Image2.tif Image3.tif Image4.tif Image5.tif Image6.tif Image7.tif isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isogr1.ent isogr2.ent isogr3.ent isogr4.ent isolat1.ent isolat2.ent isomfrk.ent isomopf.ent isomscr.ent isonum.ent isopub.ent isotech.ent mathml2.dtd mathml2-qname-1.mod mmlalias.ent mmlextra.ent soextblx.dtd us-application-body.xsl wipo.ent
application-body-pdf-wrap	ABXspectxn108-pdf-wrap.xml
abstract-pdf	ABXspectxn108-abst.pdf
claims-pdf	ABXspectxn108-clms.pdf
description-pdf	ABXspectxn108-desc.pdf
drawings-pdf	ABXspectxn108-draw.pdf

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Comments

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